## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

## **Listing of Claims:**

1. (Currently Amended) A method of processing errors in a computer system, having at least one processor, memory, and a bus coupled between the memory and the processor, comprising:

identifying, by a service processor, failed hardware of the computer system;

identifying, by the service processor, other hardware affected by the failed hardware within the computer system, wherein the other hardware comprises all functional units associated with a common bus interface error;

deconfiguring the failed hardware and the other hardware affected by the failed hardware; and rebooting the computer system without running a diagnostic on the failed hardware.

- 2. (Previously Presented) The method of claim 1, wherein the deconfiguring and rebooting steps are performed by the service processor.
- 3. (Canceled)
- 4. (Original) The method of claim 1, wherein the step of deconfiguring includes activating at least one switch of circuitry of the computer system such that the failed hardware is excluded from the computer system.
- 5. (Currently Amended) The method of claim 1, wherein the service processor identifies the failed hardware in a table entry in a second memory indicating that the failed hardware has an error, and wherein the <u>other</u> hardware affected by the failed hardware is further identified in the table indicating [[it]] the other hardware is associated with the failed hardware.
- 6. (Currently Amended) A computer system, having at least one processor, memory, and a bus coupled between the memory and the processor, comprising:
  - a plurality of hardware units connected to the computer system by the bus;
  - a service processor having firmware;

wherein when a first hardware unit of the plurality experiences an error, the first hardware unit is disconnected from the bus, wherein the error comprises a common bus interface error, and wherein a set

of additional functional units associated with the common bus interface error are also disconnected from the bus; and

wherein the computer system is restarted without running a first diagnostic associated with the first hardware unit and the set of functional units.

- 7. (Original) The computer system of claim 6, wherein the firmware of the service processor activates switches in circuitry of the computer system to disconnect the first hardware unit from the bus.
- 8. (Original) The computer system of claim 6, wherein a table is updated with information indicating the first hardware unit of the plurality has an error associated therewith.
- 9. (Canceled)
- 10. (Currently Amended) A computer program product in a computer readable medium, comprising: A recordable type medium containing a computer program product for processing errors in a computer system having at least one processor, memory, and a bus coupled between the memory and the processor, and a first hardware unit connected to the computer system, wherein the computer program product comprises:

a computer system having at least one processor, memory, a bus coupled between the memory and the processor, and a first hardware unit connected to the computer system;

first instructions for detecting, by a service processor, when an error occurs associated with the first hardware unit of the computer system, wherein the error comprises a common bus interface error;

second instructions for disconnecting the first hardware unit from a bus of the computer system, wherein the second instructions further comprise instructions for disconnecting from the bus a set of additional functional units associated with the common bus interface error;

third instructions for indicating in a table entry in a second memory that the first hardware unit has an error associated therewith; and

fourth instructions for, when the computer system is rebooted, rebooting the computer system without running a first diagnostic associated with the first hardware unit <u>and the set of additional functional units</u>.

11. (Currently Amended) The product of claim 10, wherein the second instructions [[are]] <u>further</u> <u>comprise</u> firmware associated with a service processor of the computer system.

- 12. (Canceled)
- 13. (Original) The product of claim 10, wherein disconnecting the first hardware unit includes activating at least one switch of circuitry of the computer system such that the first hardware is excluded from the computer system.
- 14. (Currently Amended) The product of claim [12] 10, wherein the service processor identifies the first hardware unit in a table entry in a second memory indicating that the first hardware unit has an error, and wherein the set of additional functional units are second hardware affected by the failed hardware is identified in the table indicating [[it is]] that the set of additional functional units are associated with the failed hardware.
- 15. (Canceled)
- 16. (Previously Presented) The method of claim 5, further comprising: responsive to the failed hardware no longer having the error, resuming operation by the failed hardware and other hardware associated with the failed hardware.
- 17. (Canceled)
- 18. (Previously Presented) The computer system of claim 6, further comprising:
  responsive to the first hardware unit no longer having the error, resuming operation by the first hardware unit and other hardware associated with the first hardware unit.
- 19. (Canceled)
- 20. (Previously Presented) The computer program product of claim 10, further comprising: responsive to the first hardware unit no longer having the error, resuming operation by the first hardware unit and other hardware associated with the first hardware unit.
- 21. (New) The method of claim 1 further comprising: persistently deconfiguring an I/O hub adapter.

| 22.     | (New) The computer system of claim 6 wherein the computer system further has an I/O hub |
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| adapter | that is persistently deconfigured.  |

23. (New) The recordable type medium of claim 10 further comprising: fifth instructions for persistently deconfiguring an I/O hub adapter.